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DATE MAILED: 05/25/2004

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/448,856	11/23/1999	ASHRAF W. LOTFI	LOTFI-22-2	5530
27964 7	590 05/25/2004		EXAMINER	
HITT GAINE	S P.C.		NADAY	/, ORI
P.O. BOX 8325	570			·
RICHARDSON, TX 75083			ART UNIT	PAPER NUMBER
			2811	

Please find below and/or attached an Office communication concerning this application or proceeding.

·	Application No.	Applicant(s)	
Office Action Comme	09/448,856	LOTFI ET AL.	GN
Offic Action Summary	Examin r	Art Unit	· · · · · · · · · · · · · · · · · · ·
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Th MAILING DATE of this communication app	pears on the cover sheet with the	correspondence add	ress
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be a within the statutory minimum of thirty (30) do will apply and will expire SIX (6) MONTHS from the application to become ABANDON	timely filed ays will be considered timely. In the mailing date of this com	imunication.
Status	•		
3) Since this application is in condition for allowar	action is non-final. nce except for formal matters, p		nerits is
closed in accordance with the practice under E	:x рапе Quayle, 1935 С.D. 11, 4	153 O.G. 213.	
Disposition of Claims	. Yi	* *	•
4) Claim(s) 44-54 is/are pending in the application	η.		
4a) Of the above claim(s) is/are withdraw	wn from consideration.		
5) Claim(s) is/are allowed.			•
6)⊠ Claim(s) <u>44-54</u> is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction and/or	r election requirement.		•
Application Papers			
9) The specification is objected to by the Examine	Γ.		
10)⊠ The drawing(s) filed on 23 November 1999 is/ar		ted to by the Examin	er.
Applicant may not request that any objection to the o	drawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the correcti	on is required if the drawing(s) is of	bjected to. See 37 CFR	1.121(d).
11)☐ The oath or declaration is objected to by the Ex	aminer. Note the attached Office	e Action or form PTO	-152.
Pri rity under 35 U.S.C. § 119	•		
 12) ☐ Acknowledgment is made of a claim for foreign a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority documents 		n)-(d) or (f).	
2. Certified copies of the priority documents		ion No	,
3. Copies of the certified copies of the priori			200
application from the International Bureau			age .
* See the attached detailed Office action for a list of		ed.	•
Attachment(s)		<u>na produce de la companya de la como</u>	<u></u>
1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)	
Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No(s)/Mail Di 5) Notice of Informal F 6) Other:		52)
S. Patent and Trademark Office			

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DETAILED ACTION

In view of the appeal brief filed on 4/7/2004, PROSECUTION IS HEREBY REOPENED. A new rejection is set forth below.

To avoid abandonment of the application, appellant must exercise one of the following two options:

- (1) file a reply under 37 CFR 1.111 (if this Office action is non-final) or a reply under 37 CFR 1.113 (if this Office action is final); or,
 - (2) request reinstatement of the appeal.

If reinstatement of the appeal is requested, such request must be accompanied by a supplemental appeal brief, but no new amendments, affidavits (37 CFR 1.130, 1.131 or 1.132) or other evidence are permitted. See 37 CFR 1.193(b)(2).

Drawings

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, a semiconductor device comprising a silicon carbide tub located within a trench in a conductive substrate and a CMOS device formed in the conductive substrate and having a tub comprising a material different from the silicon carbide tub, as recited in claim 44, must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Specification

The specification is objected to as failing to provide proper antecedent basis for the claimed subject matter. See 37 CFR 1.75(d)(1) and MPEP § 608.01(o). Correction of the following is required: an embodiment which recites a semiconductor device comprising a silicon carbide tub located within a trench in a conductive substrate and a CMOS device formed in the conductive substrate and having a tub comprising a material different from the silicon carbide tub, as recited in claim 44.

Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 44-53 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the

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invention. Applicant recites in the embodiment of figures 2A-2C a method of forming a silicon carbide tub by growing a silicon carbide layer 210 over the entire substrate 205, and then etching layer 210 to form tub 210 (see figure 2C). There is no adequate description in the disclose how the silicon carbide layer 210 is grown in trench 209, as recited in the embodiment of figure 2F, in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 44-54 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The claimed limitations of a MOSFET including a CMOS device, as recited in claims 44 and 54, are unclear as to how a MOSFET can include a CMOS device.

The claimed limitations of a tub comprising a material different from the silicon carbide tub, as recited in claims 44 and 54, are unclear as to how a material can be different than a tub.

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Claim Rej ctions - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

1. Claims 44-54 are rejected under 35 U.S.C. 103(a) as being unpatentable over Takasu (5,326,991) in view of Hu et al. (5,489,792) and Suzuki (4,896,194).

Regarding claims 44 and 54, Takasu teaches in figure 7 and related text a lateral

MOSFET including a silicon carbide tub 22b located within or contacting a conductive substrate, a gate 32b formed on the silicon carbide tub and source and drain regions located in the silicon carbide tub and laterally offset from the gate.

Takasu does not teach a CMOS device formed on the conductive substrate and having a tub comprising a material different from the silicon carbide tub.

Hu et al. teach in figure 7 an SOI device comprising a CMOS device formed on the conductive substrate 70 and having a tub comprising a material different from the silicon carbide tub (column 6, lines 38-41).

Suzuki teaches in figure 2b a CMOS device 39, 40 formed on the conductive substrate 32 and having a tub comprising a material different from the silicon carbide tub. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to form a CMOS device on the conductive substrate of Takasu in order to provide better protection for the device. The combination is motivated by the

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teachings of Hu et al. who point out the advantages of using an ESD device on the conductive substrate (column 6, lines 30-63).

Regarding claim 44, the process limitations of forming a silicon carbide tub in a trench, these would not carry patentable weight in this claim drawn to a structure, because distinct structure is not necessarily produced. Note that a "product by process" claim is directed to the product per se, no matter how actually made, In re Hirao, 190 USPQ 15 at 17 (footnote 3). See also In re Brown, 173 USPQ 685; In re Luck, 177 USPQ 523; In re Fessmann, 180 USPQ 324; In re Avery, 186 USPQ 161; In re Wertheim, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); and In re Marosi et al., 218 USPQ 289, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims or not. Note that the applicant has the burden of proof in such cases, as the above case law makes clear.

Regarding claims 45 and 46, it is known in the art that the breakdown voltage of silicon carbide is greater than that of that of a silicon, such that a MOSFET formed in a silicon carbide has a breakdown voltage of 10 volts, and a CMOS formed in a silicon has a breakdown voltage of about 3 and 5 volts.

Regarding claim 47, prior art does not teach using the MOSFET and the CMOS device as a power switch for a power converter. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use the MOSFET and the CMOS device of Takasu, Hu et al. and Suzuki as a power switch for a power converter in order to use the device in an application which requires a power switch and a power converter. Note that a recitation of the intended use of the claimed invention must result in a structural difference between the claimed invention and the prior art in order to patentably distinguish the claimed invention from the prior art. If the prior art structure is capable of performing the intended use, then it meets the claim. See *In re Casey*, 152 USPQ 235 (CCPA 1967) and *In re Otto*, 136 USPQ 458, 459 (CCPA 1963).

Regarding claim 49, Takasu teaches a tub located over the substrate.

Regarding claims 50-51, Takasu teaches a substrate and source and drain regions doped with a p or n type dopant.

Regarding claim 52, Takasu teaches a buried oxide layer 20 formed in the conductive substrate.

Regarding claim 53, Takasu does not teach a 3C SiC. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use a 3C SiC in Takasu's device, because 3C SiC is a conventional SiC, of which official notice is taken.

R spons to Argum nts

Applicant's arguments with respect to claims 44-54 have been considered but are moot in view of the new ground(s) of rejection.

Papers related to this application may be submitted to Technology center (TC) 2800 by facsimile transmission. Papers should be faxed to TC 2800 via the TC 2800 Fax center located in Crystal Plaza 4, room 4-C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (November 15, 1989). The Group 2811 Fax Center number is (703) 308-7722 and 308-7724. The Group 2811 Fax Center is to be used only for papers related to Group 2811 applications.

Any inquiry concerning this communication or any earlier communication from the Examiner should be directed to *Examiner Nadav* whose telephone number is **(571) 272-1660**. The Examiner is in the Office generally between the hours of 7 AM to 4 PM (Eastern Standard Time) Monday through Friday.

Any inquiry of a general nature or relating to the status of this application should be directed to the **Technology Center Receptionists** whose telephone number is **308-0956**

O.N. May 20, 2004

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